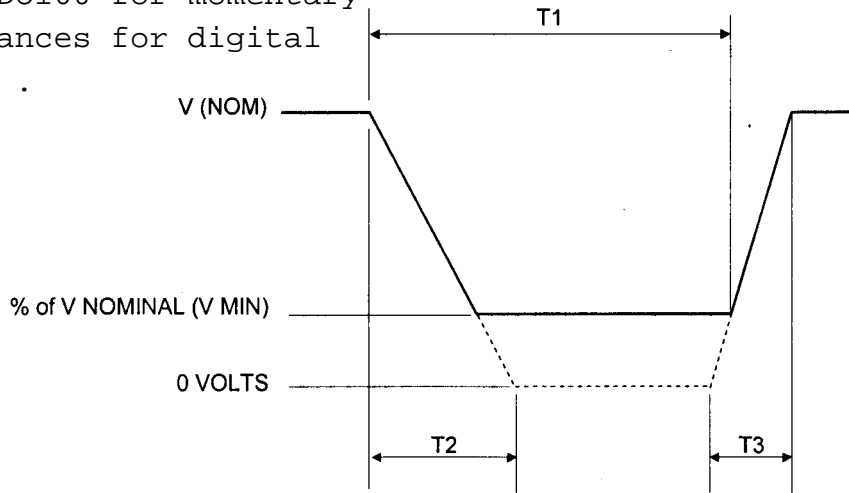


Table 16-3 Test Conditions for DC Equipment with Digital Circuits

Excerpt from D0160 for momentary power disturbances for digital circuitry . . .



Notes:

1. Definitions:

T1 Power interrupt time.

T2 Time it would take for the applied voltage to decay from *V (nom)* to zero volts.

T3 Time it would take for the applied voltage to rise from zero to *V (nom)* volts.

V MIN The minimum level (expressed as a percentage of *V NOMINAL*) to which the applied voltage is permitted to decay.

2. Tolerance to *T1*, *T2*, *T3* = ± 10%.

Applicable Category	A, B, Z				A, Z			Z	A, B, Z		A, Z				Z	A, B, Z			
	1	2*	3	4	5	6	7	8	9*	10*	11	12	13	14	15	16*	17*	18*	19
Test Condition No.	1	2*	3	4	5	6	7	8	9*	10*	11	12	13	14	15	16*	17*	18*	19
T1 (mS)	2	10	25	50	75	100	200	1000	10	25	50	75	100	200	1000	30	35	40	25
T2 (mS)	<1	20*	20	20	20	20	20	20	50*	50*	50	50	50	50	50	50*	50*	20*	5
T3 (mS)	<1	5	5	5	5	5	5	5	20	20	20	20	20	20	20	20	5	20	5
% of Vnominal (V min)	0	50	15	10	5	0	0	0	80	50	0	15	5	0	0	65	60	35	0

* Voltage will not reach zero in this test condition.